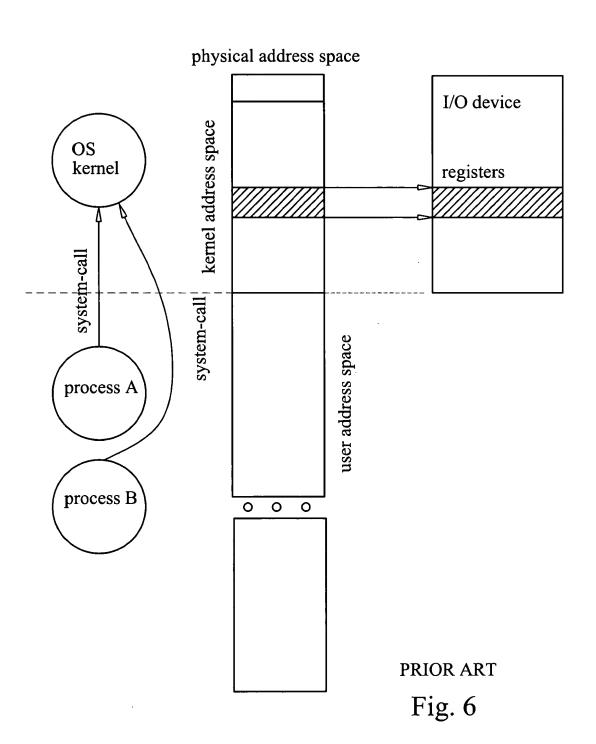




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physical address space mapping in I/O device user address space process A registers page for I/O process B 0 0 PRIOR ART Fig. 7



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